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August 17, 2001

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Our File:

Date:

In the PATENT APPLICATION of:

Drapkin et al.

**Application No.:** 

09/651,944

Filed:

August 31, 2000

For:

METHOD AND APPARATUS TO

**OPTIMIZE RECEIVING SIGNALS** 

REFLECTION

Group:

2816

Examiner:

Hiep Nguyen

REPLY PURSUANT TO 37 C.F.R. § 1.111

Commissioner for Patents Washington, D.C. 20231

Sir:

This Reply is in response to the Office Action dated March 28, 2001 and is filed in conjunction with an appropriate Petition for Extension of Time.

Without prejudice to Applicant, kindly amend the application as follows:

01/12/2001 FFANAFIA 00000022 08561944 01 704103 90.00 DP Adjustment date: 08/94/2001 FFANAEIA
08/22/2001 FFANAEIA 00000022 0 9561944
02 FC:116
-3 90.00 0P

Page 2, please delete the paragraph between lines 5 through 7 and insert therefor:

--It is therefore, one object of the present invention, to provide an apparatus for compensating for impedance mismatch between interconnected circuits operating at high

frequencies.--

CB/E4/2000 TTAMBTEN TOUSCOOL 05051964